

P.S.

At page 69, line 16, replace "real" with --physical--.

At page 74, line 12, replace "466" with --820--.

At page 78, line 24, after "action 524." insert the sentence --The ring buffer write pointer, pointing to the next location in the ring buffer, is maintained in R15 ("RingBuf" of Table 1). After the collected profile packet is stored at the location indicated by R15, R15 is postincremented by the size of a profile packet. --.

At page 83, line 10, replace "the emulator" with --emulator 316 --.

At page 87, lines 14 and 22, replace "The emulator" with --Emulator 316--.

At page 87, line 19, replace "the emulator" with --emulator 316 --.

At page 94, line 10, after "entry" insert --640--.

At page 96, line 15, replace "the emulator" with --emulator 316 --.

At page 98, line 6, replace "real" with --physical--.

At page 99, line 7, replace "it" with --the handler--.

At page 99, line 13, replace "the EPC" with --EPC.EIP--.

At page 99, line 21, replace "set" with --cleared--.

At page 102, line 19, replace "One" with --On--.

---

**In the Claims:**

Please amend the claims as follows.

1. A method, comprising the steps of:

while executing a program on a computer, detecting the occurrence of profileable events occurring in the instruction pipeline, and directing the instruction pipeline to record profile information describing the profileable events essentially concurrently with the occurrence of the profileable events, the detecting and recording occurring under control of hardware of the computer without software intervention.

6. The method of claim 5, further comprising the step of:

when an instruction fetch of an instruction causes a miss in a translation look aside buffer (TLB), the fetch of the instruction triggering a profileable event, servicing the TLB miss and reflecting the corrected state of the TLB in the profile information recorded for the profileable instruction.

7. The method of claim 5:

---